

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Shepard CONFIRMATION NO.: 4816
SERIAL NO.: 09/821,182 GROUP NO.: 2824
FILING DATE: March 29, 2001 EXAMINER: Andrew Q. Tran
TITLE: DUAL-ADDRESSED RECTIFIER STORAGE DEVICE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBSTITUTE REISSUE OATH/DECLARATION OF SOLE INVENTOR

Sir:

I, Daniel R. Shepard, declare that:

- (1) My residence, post office address, and citizenship are as stated below next to my name;
- (2) I believe that I am the sole, original and the first inventor of the subject matter which is claimed in the above-referenced application for reissue and for which a patent is sought;
- (3) I believe that the original patent is wholly or partially inoperative by reason of my claiming less than I had the right to claim and that this error is being relied upon as the basis for filing this reissue application;
- (4) Independent claims 1, 14, and 25 of the original patent and claims dependent thereon include "means for selecting a conductive means of one plurality of generally parallel conductive means, and means for biasing the generally parallel conductive means of the other plurality of generally parallel conductive means such that each said rectifying conductive means present between a conductive means of said biased plurality of generally parallel conductive means to a conductive means of the other said plurality of generally parallel conductive means is potentially forward biased" and "means for selecting a biased conductive means by electronically disabling conductive means within said biased plurality of generally parallel conductive means by shifting the voltage of those biased

conductive means that are to be disabled," and these limitations are not required for patentability;

- (5) An error being relied upon as the basis for this reissue application, under 37 C.F.R. §1.175(a)(1), is that the scope of claims 1, 14, and 25 as issued was too narrow in that my invention is broad enough to embrace "address circuitry comprising a first pattern of rectifiers directly connected between the first set of conductive lines and a first set of address signal lines, application of an address to the first set of address signal lines causing the first pattern of rectifiers to disable all but one of the first set of conductive lines," as recited in new independent claim 31, which omits the claim limitations listed above in (4);
- (6) The claims of this reissue application are broadened;
- (7) I have reviewed and understand the contents of the specification of this reissue, including the new claims referred to in this Oath/Declaration, as well as the amendment submitted herewith and amendments previously submitted;
- (8) All errors being corrected in this reissue application up to the time of filing of this oath/declaration arose without any deceptive intention on my part;
- (9) I acknowledge the duty to disclose to the Office all information known to me to be material to patentability as defined in 37 CFR 1.56; and
- (10) I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.



Sole Inventor: Daniel R. Shepard

Date: Nov 16, 2009

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